

FIG. 1 is a block diagram of a system for monitoring a user location. The system includes a user location 15, a sensor device 10, a wireless device 50, a local telco 55, a telco computer 65, the internet 40, and a central monitoring unit 30. The user location 15 is connected to the sensor device 10, which is connected to the wireless device 50. The wireless device 50 is connected to the local telco 55. The local telco 55 is connected to the telco computer 65, which is connected to the internet 40. The internet 40 is connected to the central monitoring unit 30.

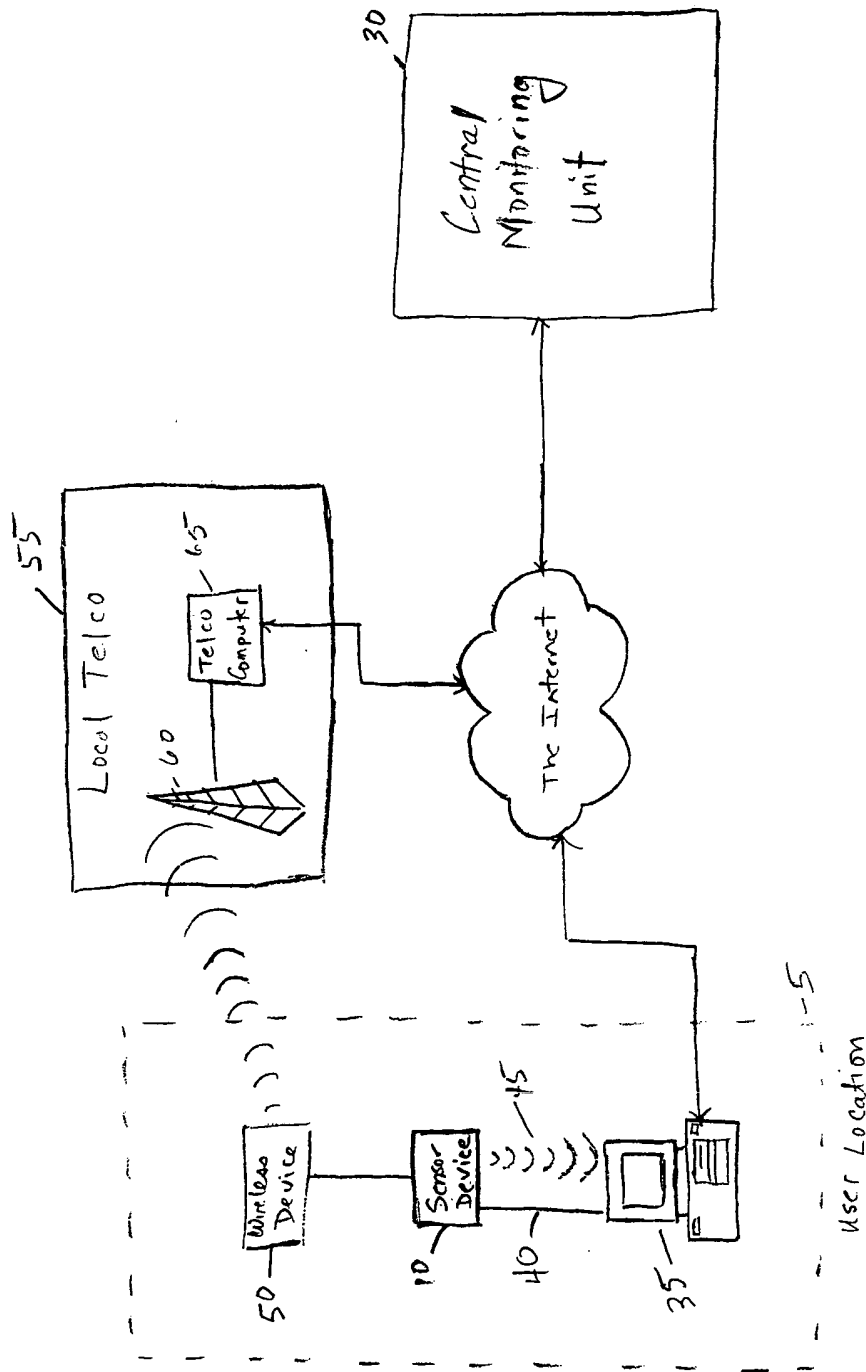


Fig. 1

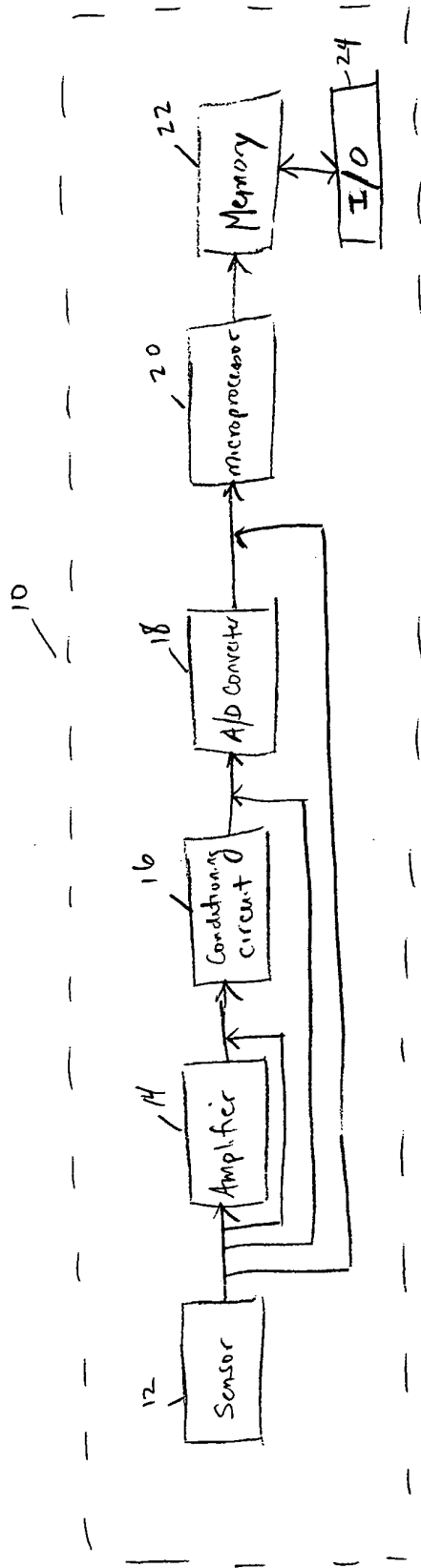


Fig. 2

Figure 3 is a block diagram of a network architecture. The network architecture includes a Router (75), a Firewall (80), a Switch (85), a Load Balancer (90), and three Middleware Servers (95a, 95b, 95c). The Router (75) is connected to the Firewall (80), which is connected to the Switch (85). The Switch (85) is connected to the Load Balancer (90), which is connected to the three Middleware Servers (95a, 95b, 95c). The Switch (85) is also connected to a Database Server (110), which is connected to Network Storage (100). The Network Storage (100) is connected to a client (115).

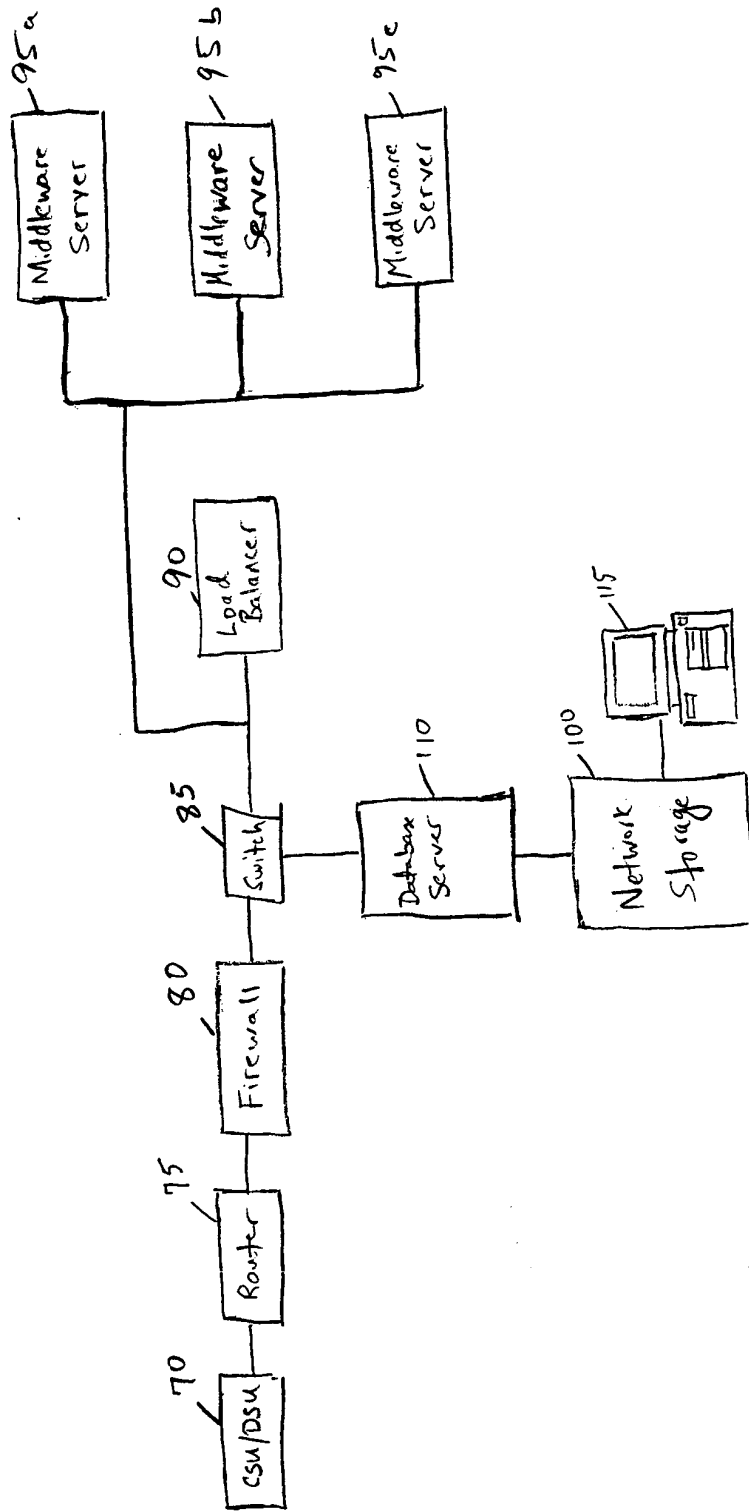


Fig. 3

FIG. 4 is a block diagram of a network architecture. The network architecture includes a Router (70), a Firewall (80), a Switch (85), a Load Balancer (90), and a Load Balancer (130). The Router (70) is connected to the Firewall (80), which is connected to the Switch (85). The Switch (85) is connected to the Load Balancer (90) and the Load Balancer (130). The Load Balancer (90) is connected to three Middleware Servers (95a, 95b, 95c). The Load Balancer (130) is connected to three Middleware Servers (135a, 135b, 135c). The Switch (85) is also connected to two Database Servers (110, 125). The Database Server (110) is connected to Network Storage (100). The Database Server (125) is connected to Mirror Network Storage (120). The Network Storage (100) is connected to a computer (115). The Mirror Network Storage (120) is connected to a computer (122).

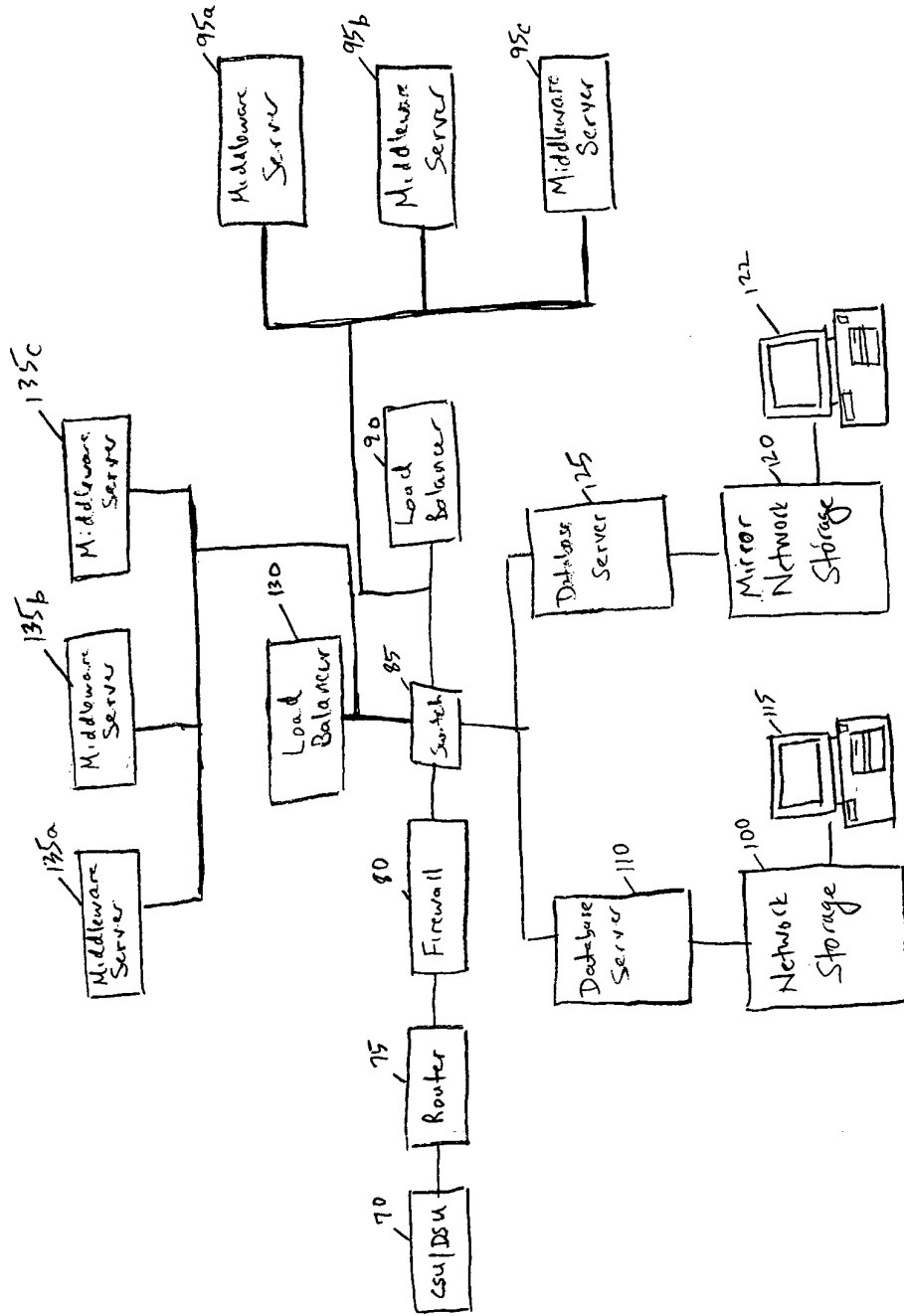


Fig 4



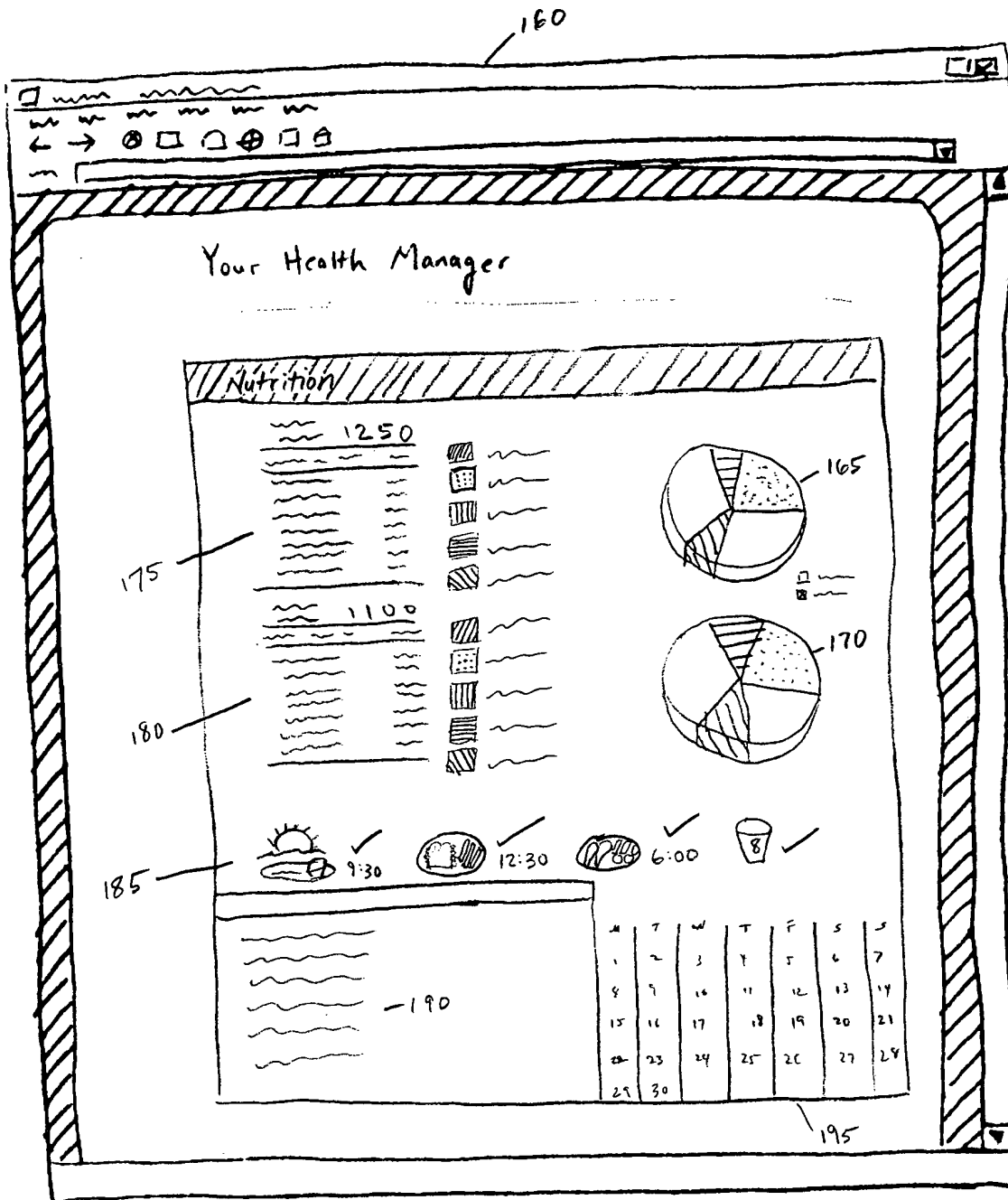


Fig. 6

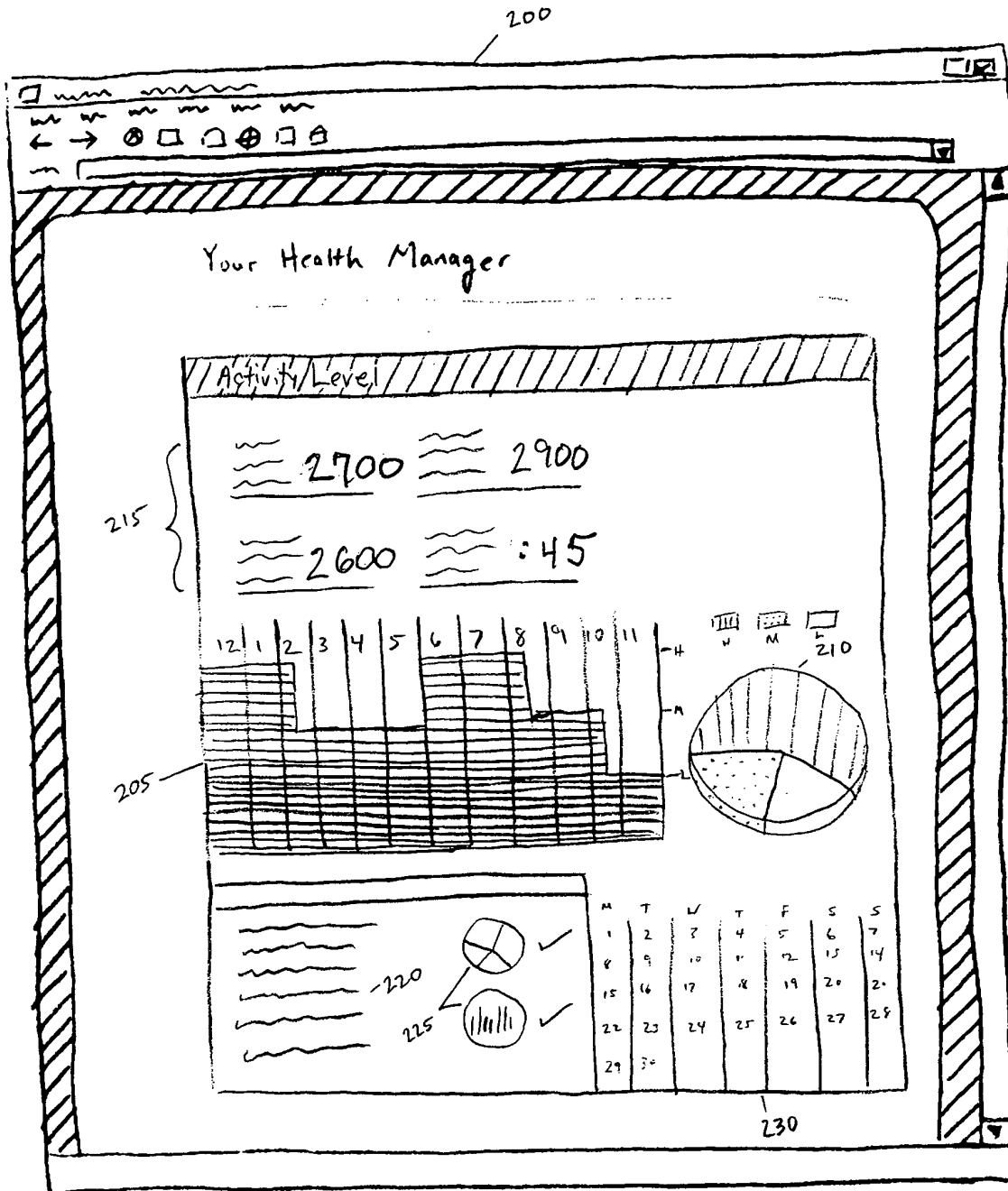


Fig. 7

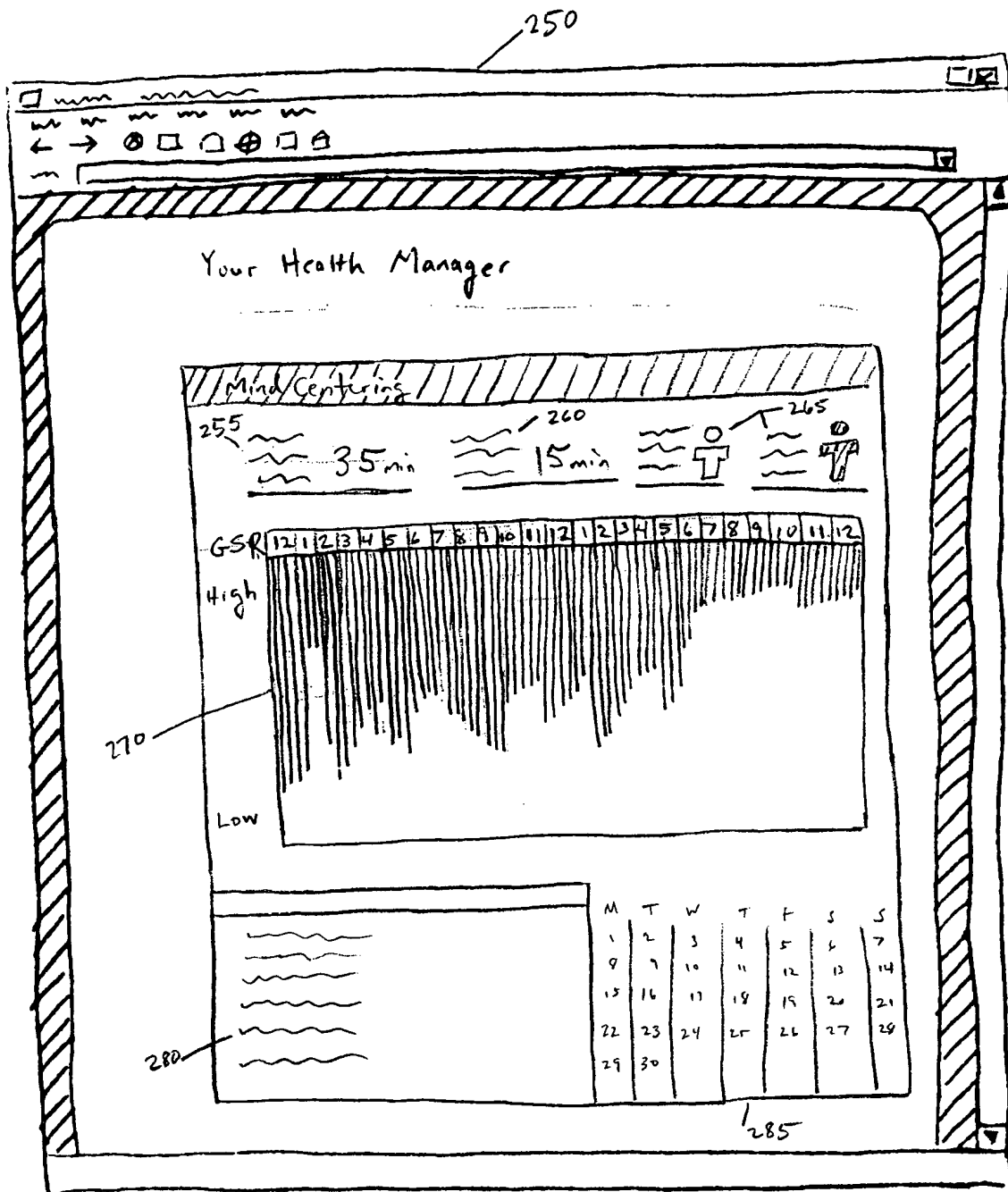


Fig. 8



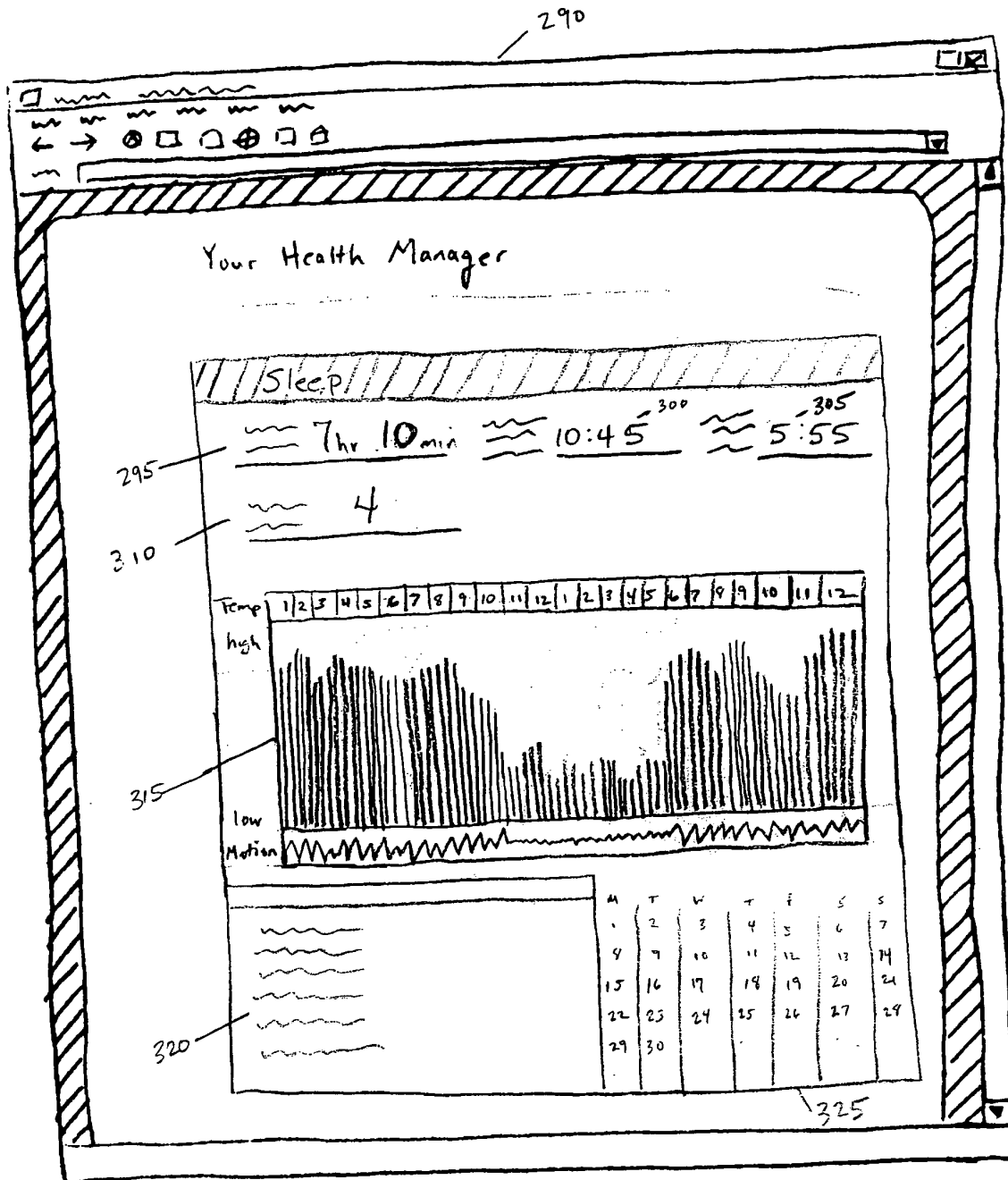


Fig. 9

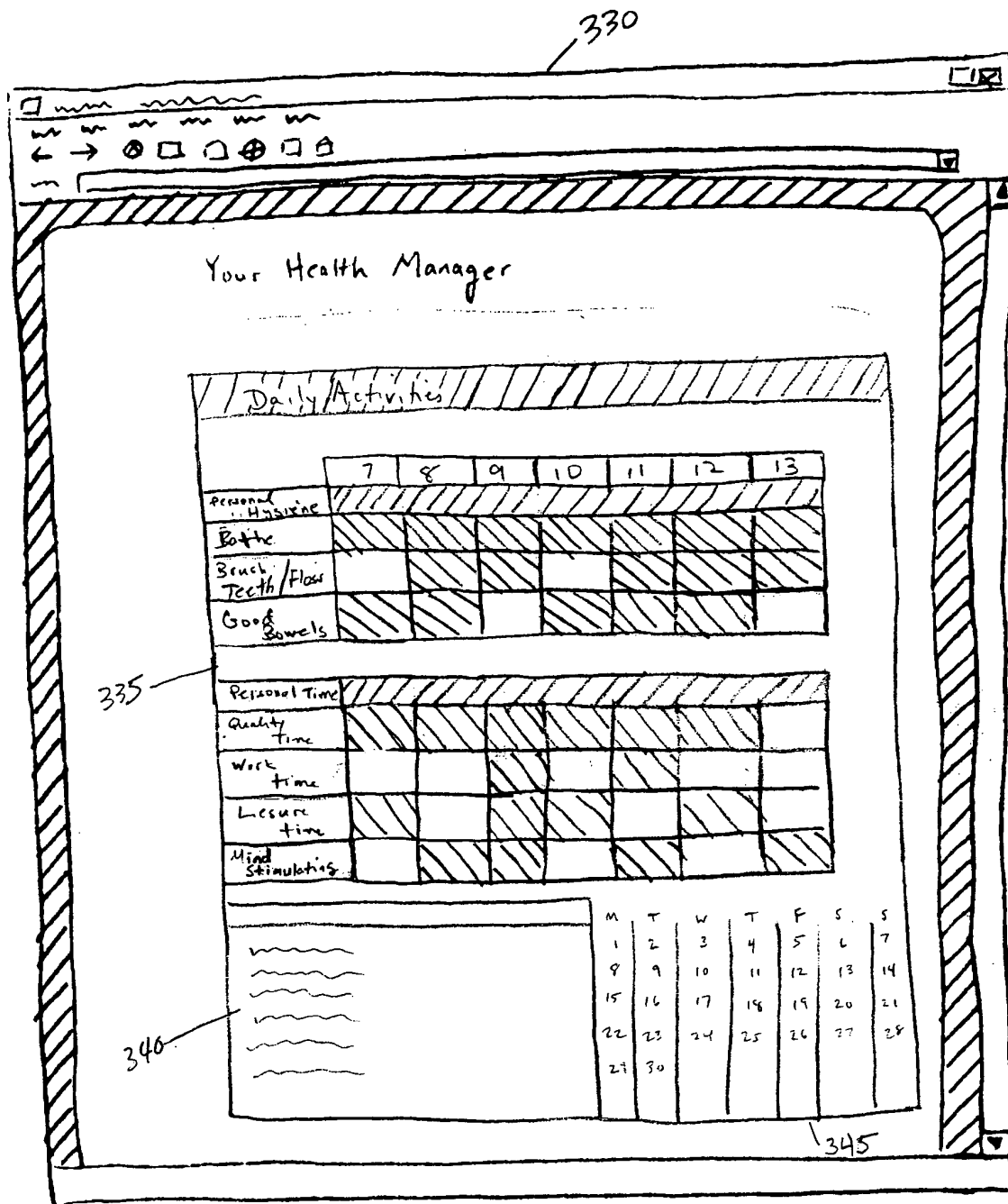


Fig. 10

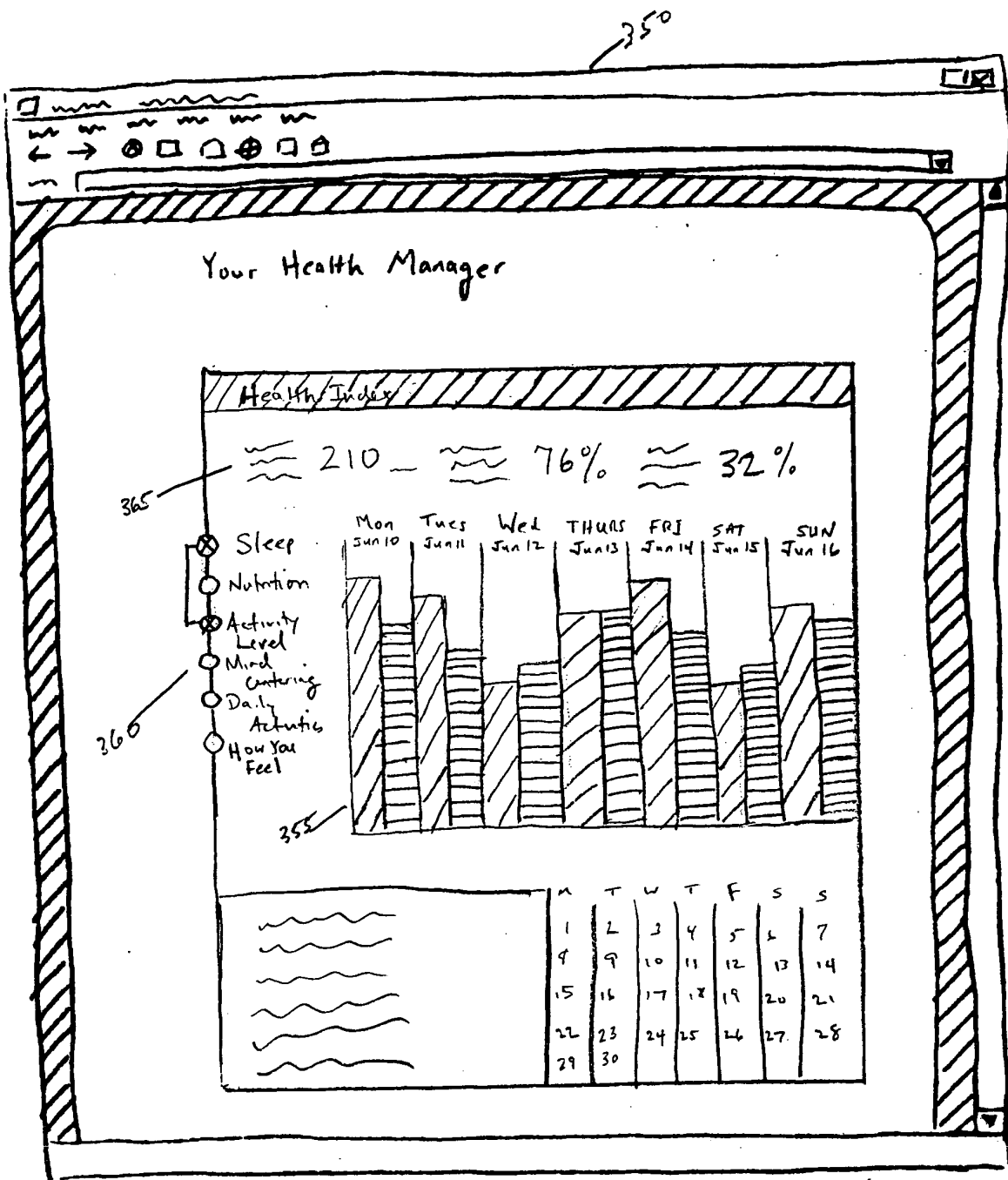


Fig. 11

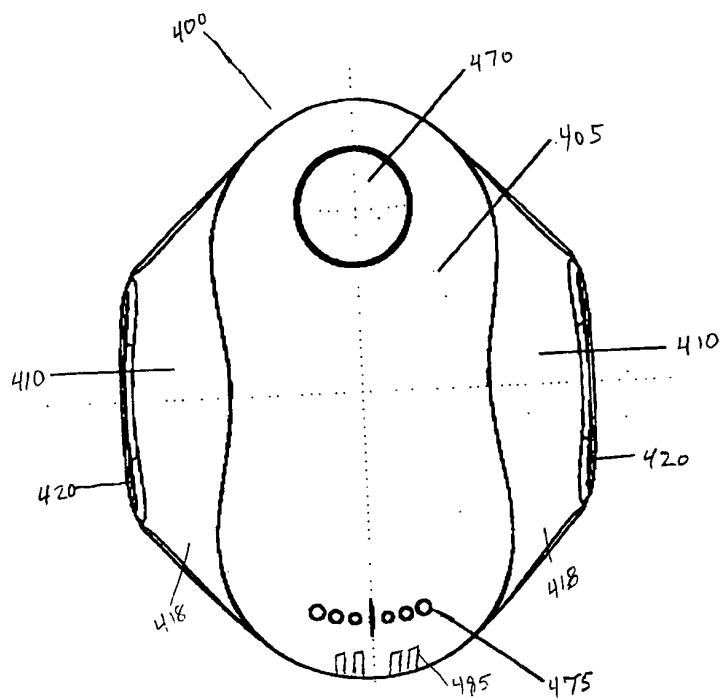
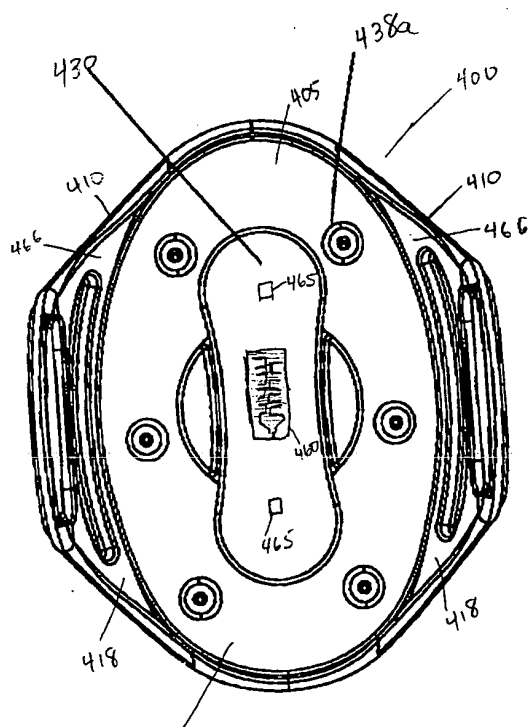


Fig. 12



440, Fig. 13

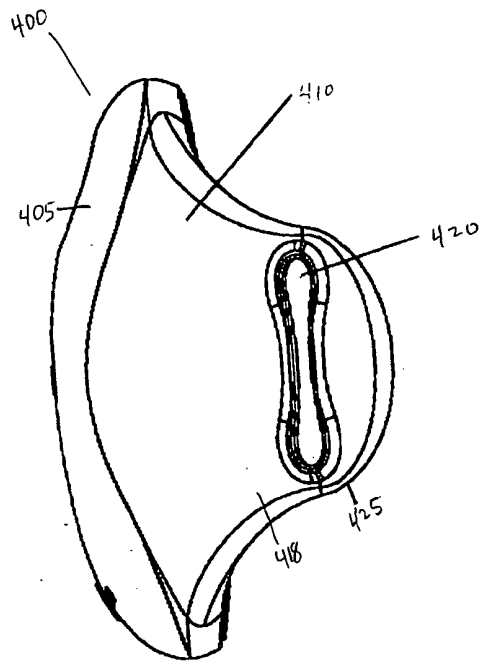


Fig. 14

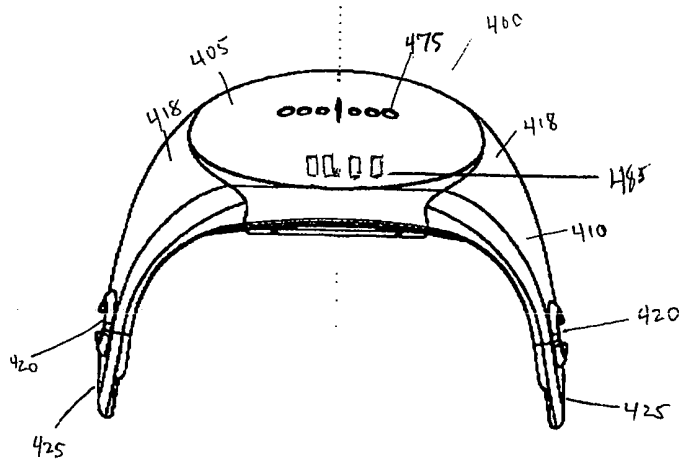


Fig. 15

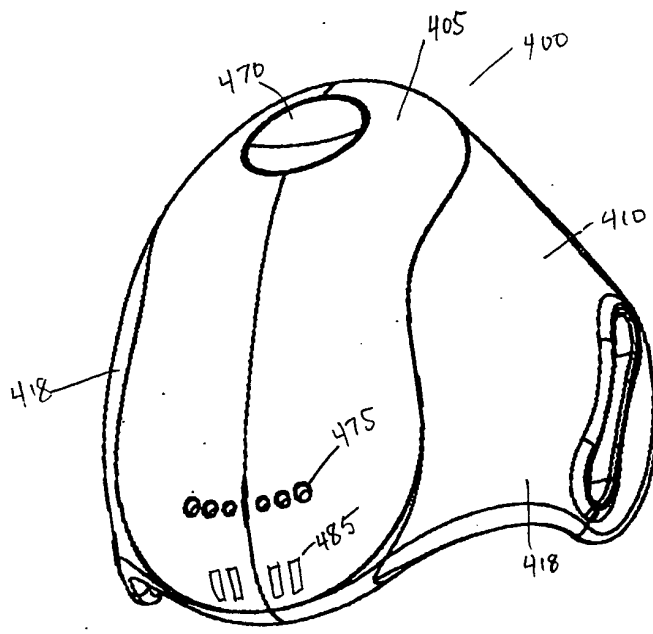


Fig. 16

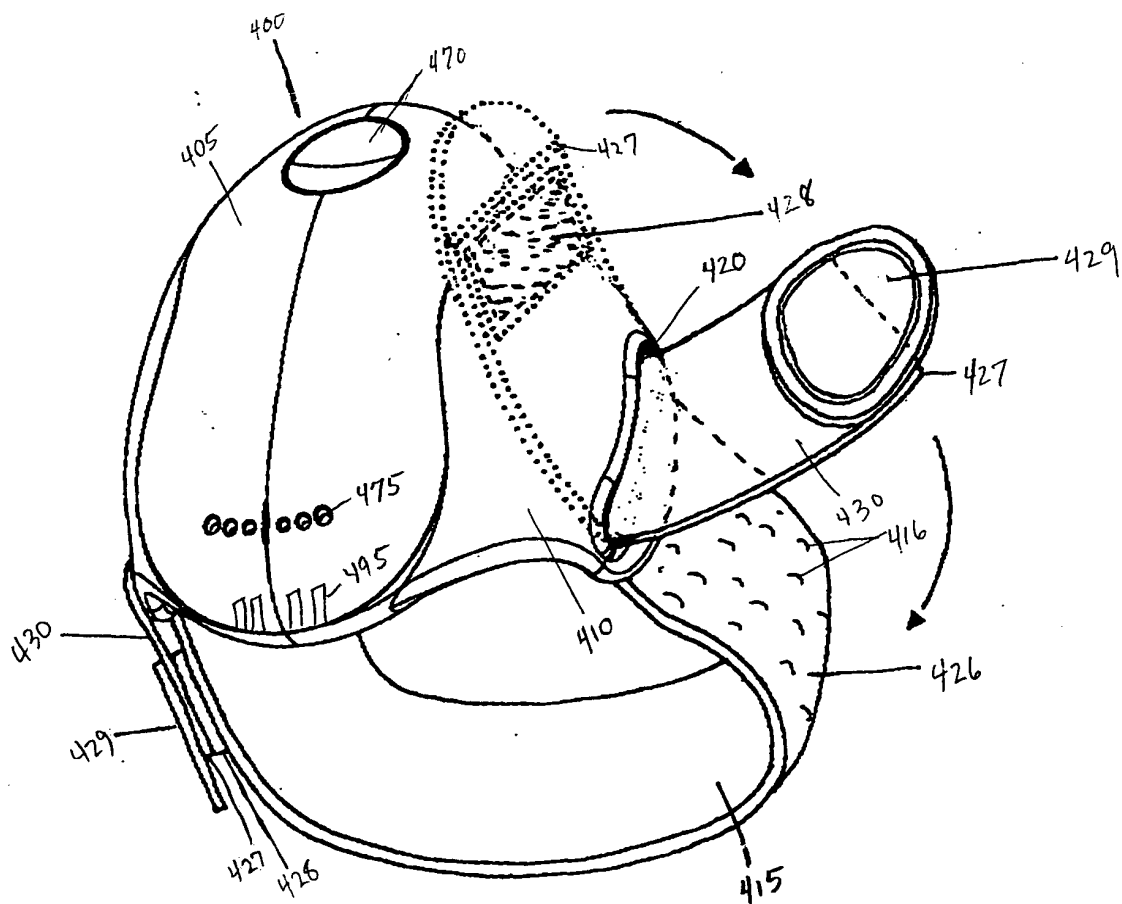


Fig. 17

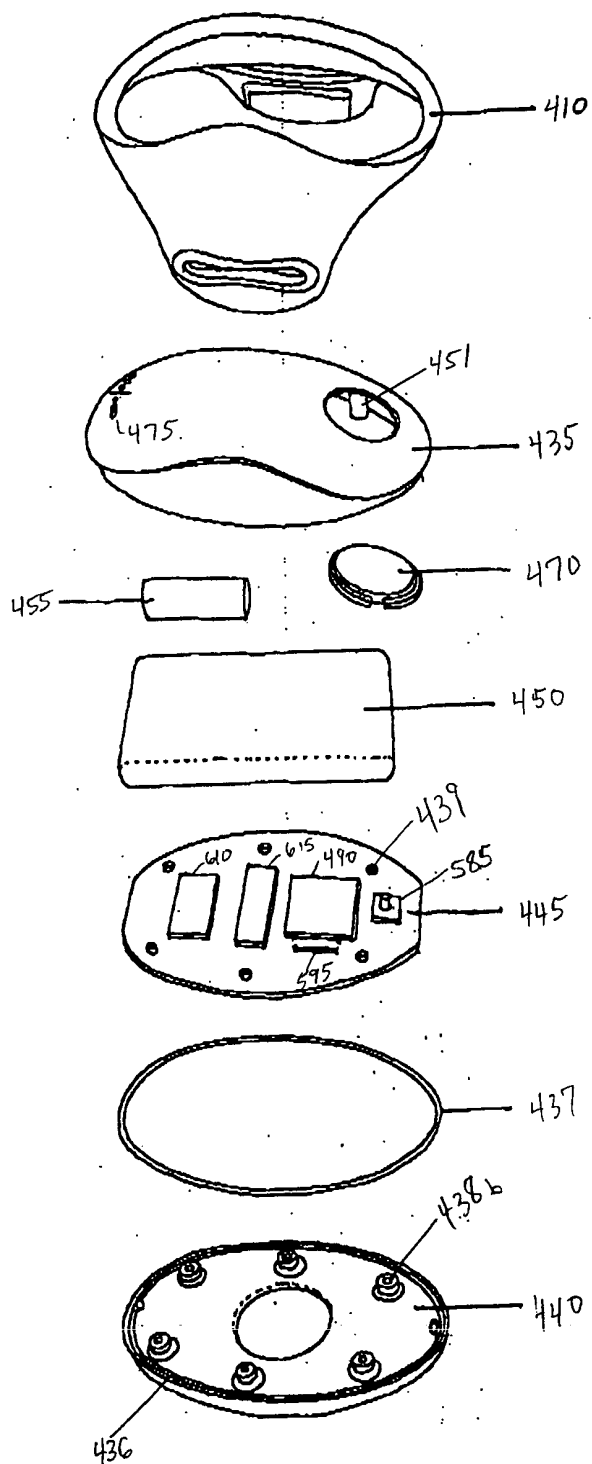


Fig. 18



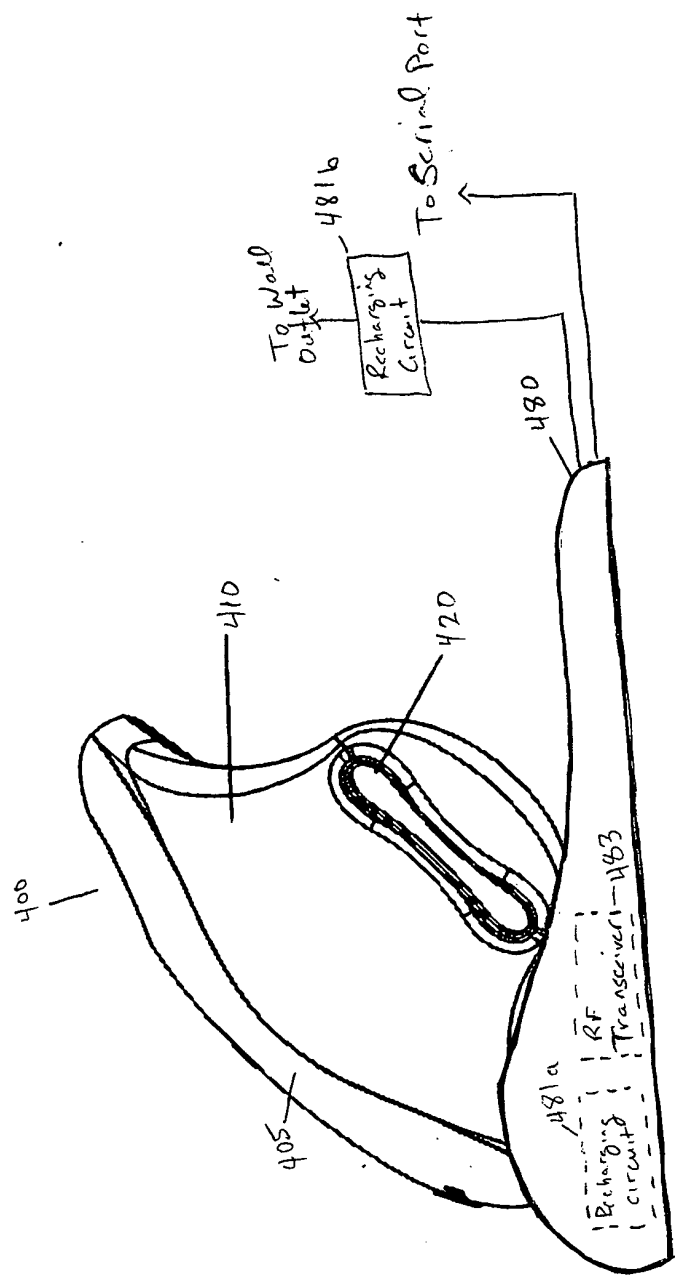


Fig. 19

FIG. 20 is a block diagram of a system 490 in accordance with the present invention. The system 490 includes a processing unit 490, a battery 450, a voltage regulator 605, an oscillator 595, a reset circuit 600, a switch 585, a LED latch driver 590, LEDs 475, a 3-axis accelerometer 550, an RF receiver 555, an RF transceiver 565, an antenna 558, a vibrator 455, a ringer 575, a driver 540, a driver 590, a battery monitor 545, an A/D converter 505, a 2-axis accelerometer 495, a buffer 500, a buffer 510, an amp/offset 515, a filter/conditioning 520, an amp 535, a filter 530, an amp 525, a heat flux sensor 460, GSR sensors 465, SRAM 610, and flash 615.

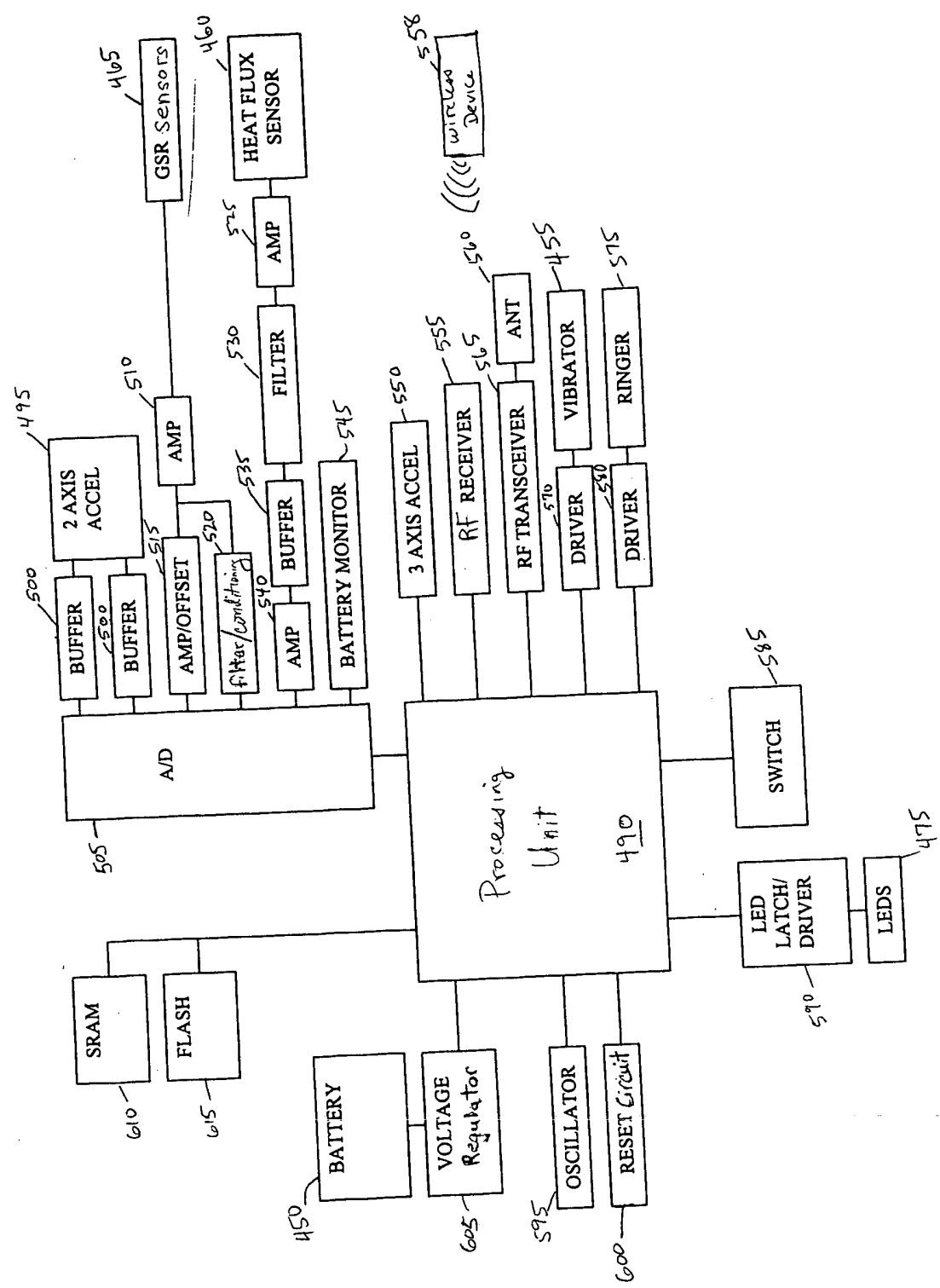


Fig. 20

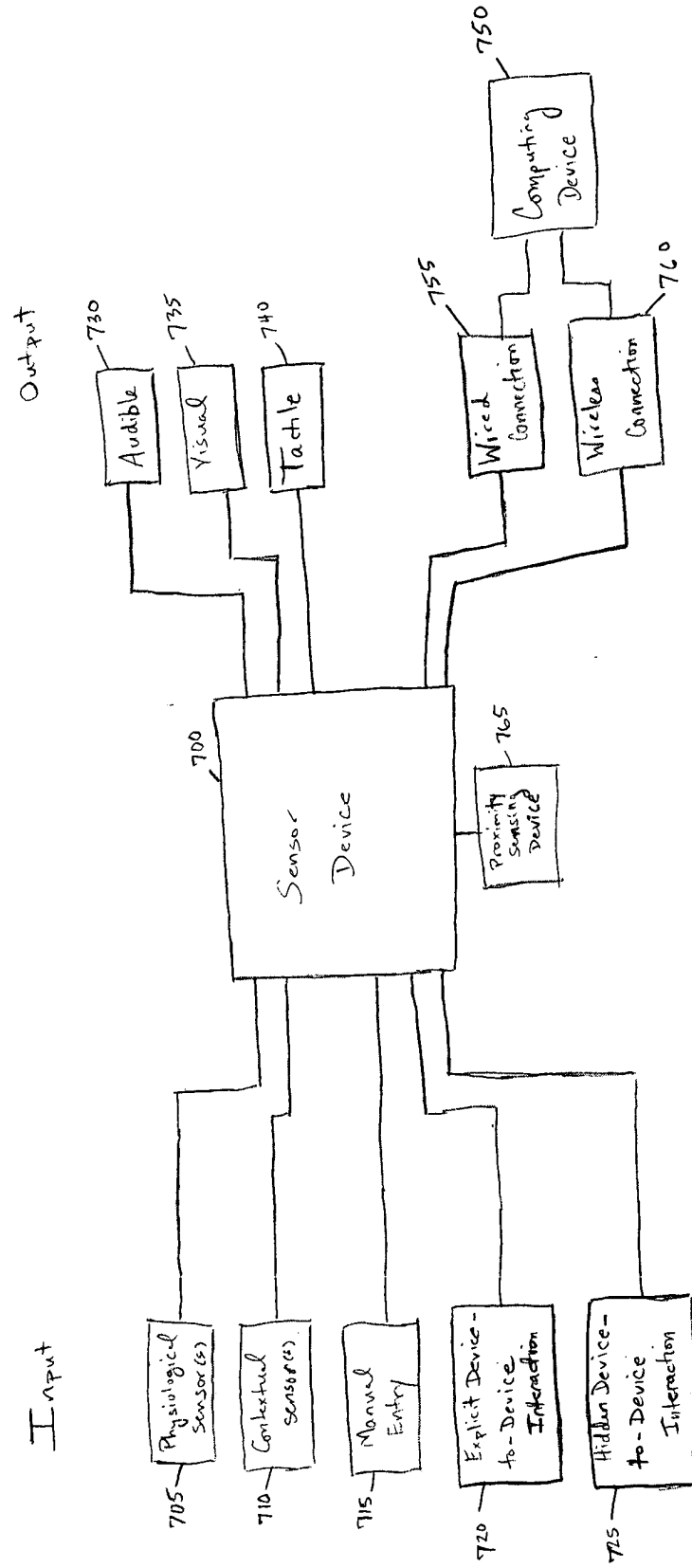


Fig 21